

HMI-200-8096/196

- Dual 4K TRACE Buffers
- Complex Event Triggering
- 64K Overlay Memory (256K & 1 Meg Optional)
- Supports INST Line
- Supports Bank Switching
- 16 External TRACE Input Leads
- 2 External Trigger Outputs
- Source-Level Debugging Software

Full in-circuit emulation for the Intel MCS[®] 96 microcontroller family including the 80196KB, KC and KD versions.

External bank switching inputs allow the target hardware to select up to 4 banks of either target or emulator overlay memory. Sixteen external inputs can be used either as input conditions for triggering or trace inputs. Complex event and triggering sequences permit precise control of the TRACE thereby reducing the requirement for large trace buffers. The optional hardware based performance analysis provides accurate timing and code coverage information. Software development is enhanced by the use of SourceGate II, a full featured source-level debugging package that is available for Windows*, SUN or HP host platforms.

MICROCONTROLLERS

SUPPORTED:

8xC196KB/KC/KD, 8xC196MC,
8xC19816

DEVELOPMENT PLATFORMS:

PC (Non-Windows), PC Windows, SUN Workstation, HP Workstation

AVAILABILITY:

Now

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For international contacts, see Appendix B.

